

ABSTRACT

An amplifier linearizer circuit has a signal cancellation circuit including a signal adjuster having M branch signals ($M \geq 1$), and a distortion cancellation circuit including a signal adjuster having N branch signals ($N \geq 1$). The
5 linearizer has a controller for adaptively controlling the M-branch and N-branch signal adjusters. The controller has only one monitor receiver to monitor the M branch signals and only one monitor receiver to monitor the N branch signals.

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